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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/742,383	12/22/2000	Moo Jin Lee	2658-0251P	9446

2292 7590 12/01/2004

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EXAMINER

DHARIA, PRABODH M

ART UNIT PAPER NUMBER

2673

DATE MAILED: 12/01/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action

Application No.

09/742,383

Applicant(s)

LEE, MOO JIN

Examiner

Prabodh M Dharja

Art Unit

2673

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 24 October 2004 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

PERIOD FOR REPLY [check either a) or b)]

- a) ☐ The period for reply expires _____ months from the mailing date of the final rejection.
- b) ☒ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection. ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1. ☐ A Notice of Appeal was filed on _____. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.
2. ☐ The proposed amendment(s) will not be entered because:
- (a) ☐ they raise new issues that would require further consideration and/or search (see NOTE below);
 - (b) ☐ they raise the issue of new matter (see Note below);
 - (c) ☐ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
 - (d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: _____.

3. ☐ Applicant's reply has overcome the following rejection(s): _____.
4. ☐ Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
5. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ request for reconsideration has been considered but does NOT place the application in condition for allowance because: _____.
6. ☐ The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.
7. ☒ For purposes of Appeal, the proposed amendment(s) a) ☐ will not be entered or b) ☒ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: 1-8.

Claim(s) objected to: _____.

Claim(s) rejected: 9-26.

Claim(s) withdrawn from consideration: _____.

8. ☒ The drawing correction filed on 22 December 2000 is a) ☒ approved or b) ☐ disapproved by the Examiner.
9. ☐ Note the attached Information Disclosure Statement(s) (PTO-1449) Paper No(s). _____.
10. ☐ Other: _____.

Art Unit: 2673

Status: Receipt is acknowledged of papers submitted on 10-14-2004 under remarks and argument, which have been placed of record in the file. Claims 1-26 are pending in this action.

Response to Amendment

2. Applicant's remarks and argument to Claim 9-26 on 09-15-2004 are not entered as they do raise new issues that would require further consideration and search, since they were not presented before final office action.

Response to Arguments

3. Applicant's arguments filed 05-04-2004 have been fully considered but they are not persuasive.

Applicant argues the cited references do not obviate.

Examiner disagrees In response to applicant's argument that there is no suggestion to combine the references, the examiner recognizes that obviousness can only be established by combining or modifying the teachings of the prior art to produce the claimed invention where there is some teaching, suggestion, or motivation to do so found either in the references themselves or in the knowledge generally available to one of ordinary skill in the art. See *In re Fine*, 837 F.2d 1071, 5 USPQ2d 1596 (Fed. Cir. 1988) and *In re Jones*, 958 F.2d 347, 21 USPQ2d 1941 (Fed. Cir. 1992). In this case, Katakura et al. teaches basic LCD operation the scan line controls data input to TFT gate input, Kikuo et al. teaches gate input is controlled specifically using resistor and thermistor and receives high voltage as claimed in claims 9-26 and Reents et al. teaches a gate line driver receiving said controlling signal from said gate line

Art Unit: 2673

controller and supplying to a gate line a voltage varied according to the controlling signal to drive the gate line. The combination teaches applicant's invention and teachings and therefore they do obviate. The gate input of the transistor controlled by combination of the resistor and thermistor for various application is well known in the art. The contrast or light transmissivity of a LCD display with respect temperature (using thermistor and resistor) is also well known in the art (see cited references of 892's). Claims 1-8 are allowed claiming, controlling light transmissivity of the LCD display with direct application of the high voltage controlled input via combination of thermistor and resistor to gate input of the TFT controlling scan line input in response to data signal from data lines.

Recited Claim: a light transmissivity of LCD display in response to data signals from the data lines, and a plurality of switching devices for switching the data signals to be applied from the data lines to the liquid crystal cells in response to signals on the gate lines, the circuit comprising: a voltage supply for generating a gate voltage required for the gate lines; a gate line driver for applying the gate voltage from the voltage supply to the gate lines to drive the gate lines; and a current controller including a resistor and a thermistor for responding to a change in the ambient temperature to change an amount of current of the gate voltage to be applied from the voltage supply to the gate line driver, thereby changing a width of a current path from the data line to the liquid crystal cell.

The independent claims of claim 9-26 fail to claim or recite above underlined and bold.

Art Unit: 2673

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Prabodh M Dharia whose telephone number is 703-605-1231.

The examiner can normally be reached on M-F 8AM to 5PM.

5. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala can be reached on 703-3054938. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

6. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Any response to this action should be mailed to:

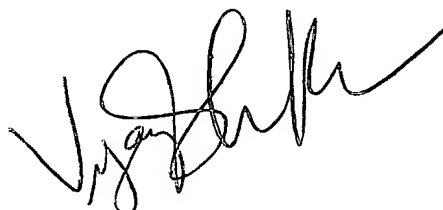
Commissioner of Patents and Trademarks

Washington, D.C. 20231

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November 03, 2004



VIJAY SHANKAR
PRIMARY EXAMINER